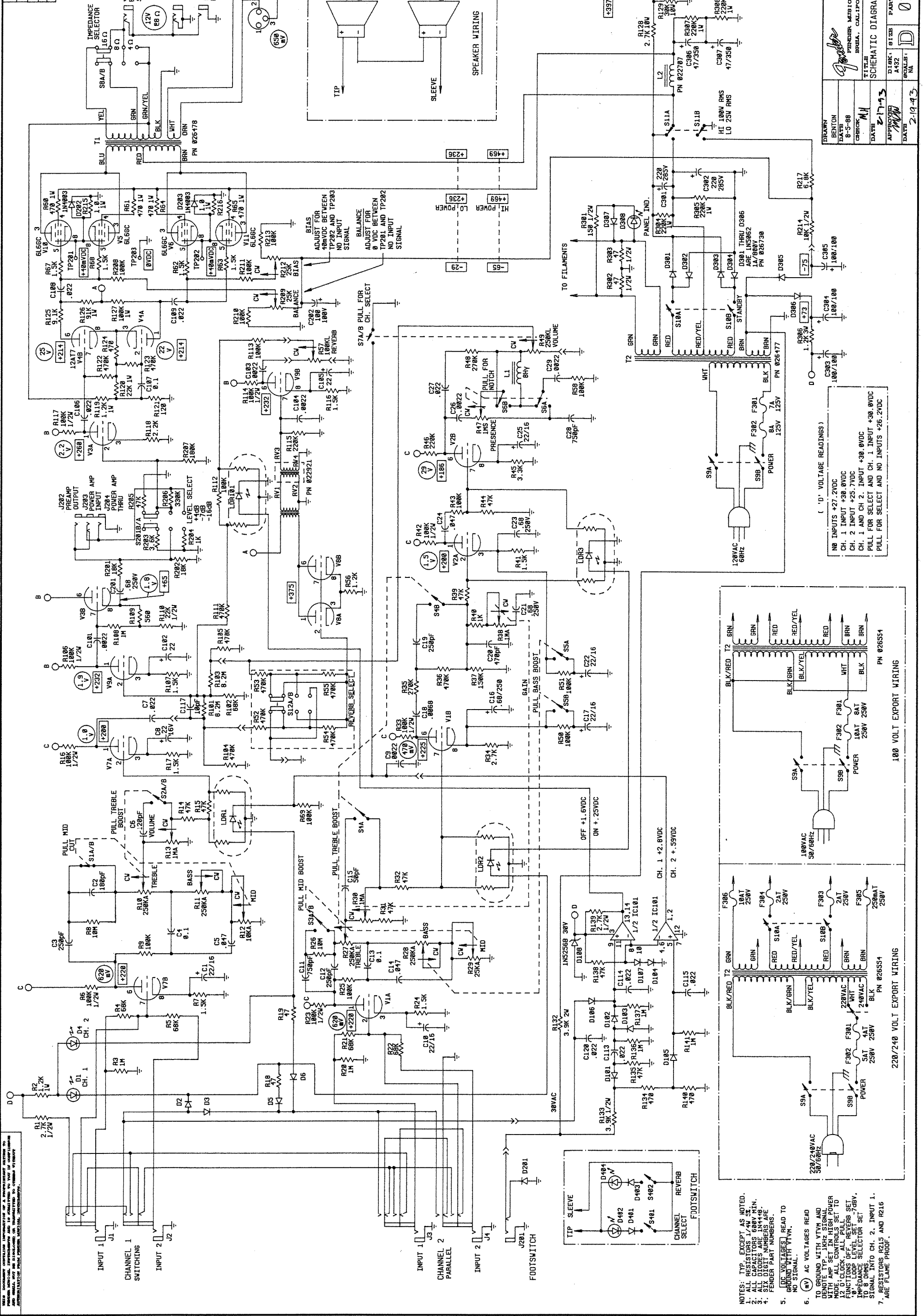


REV	DATE	BY
J	REDRAWN EC# 325	BB
K	EC# 101659	BB
L	EC# 414	BB
M	EC# 58	BB
N	EC# 102592	BB
O	EC# 102592	BB



REV.	N
DATE	2-19-43
APPROVED	BY
DESIGNED	BY
CHECKED	BY
DATE	2-19-43
TITLE	SCHEMATIC DIAGRAM 'THE TWIN'
PROJECT	026059
REV.	N

NOTES: TYP. EXCEPT AS NOTED.
 1. ALL CAPACITORS 50V MIN.
 2. ALL DIODES ARE 1N44B.
 3. ALL SIX DIGIT NUMBERS ARE FENDER PART NUMBERS.
 4. [DC VOLTAGES] READ TO GROUND WITH VTVM.
 5. AC VOLTAGES READ TO GROUND WITH VTVM AND DETECTIVE TYPE HIGH SIGNAL WITH AMP SET IN HIGH POWER MODE; ALL CONTROLS SET TO 12 O'CLOCK (ALL PULLING SET TO 12 O'CLOCK).
 6. LOOP LEVEL SET TO 7.5dBV.
 7. IMPEDANCE SELECTOR SET TO 8 OHMS.
 8. SIGNAL INTO CH. 2, INPUT 1.
 9. ARE FLAME PROOF.

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REV.	N