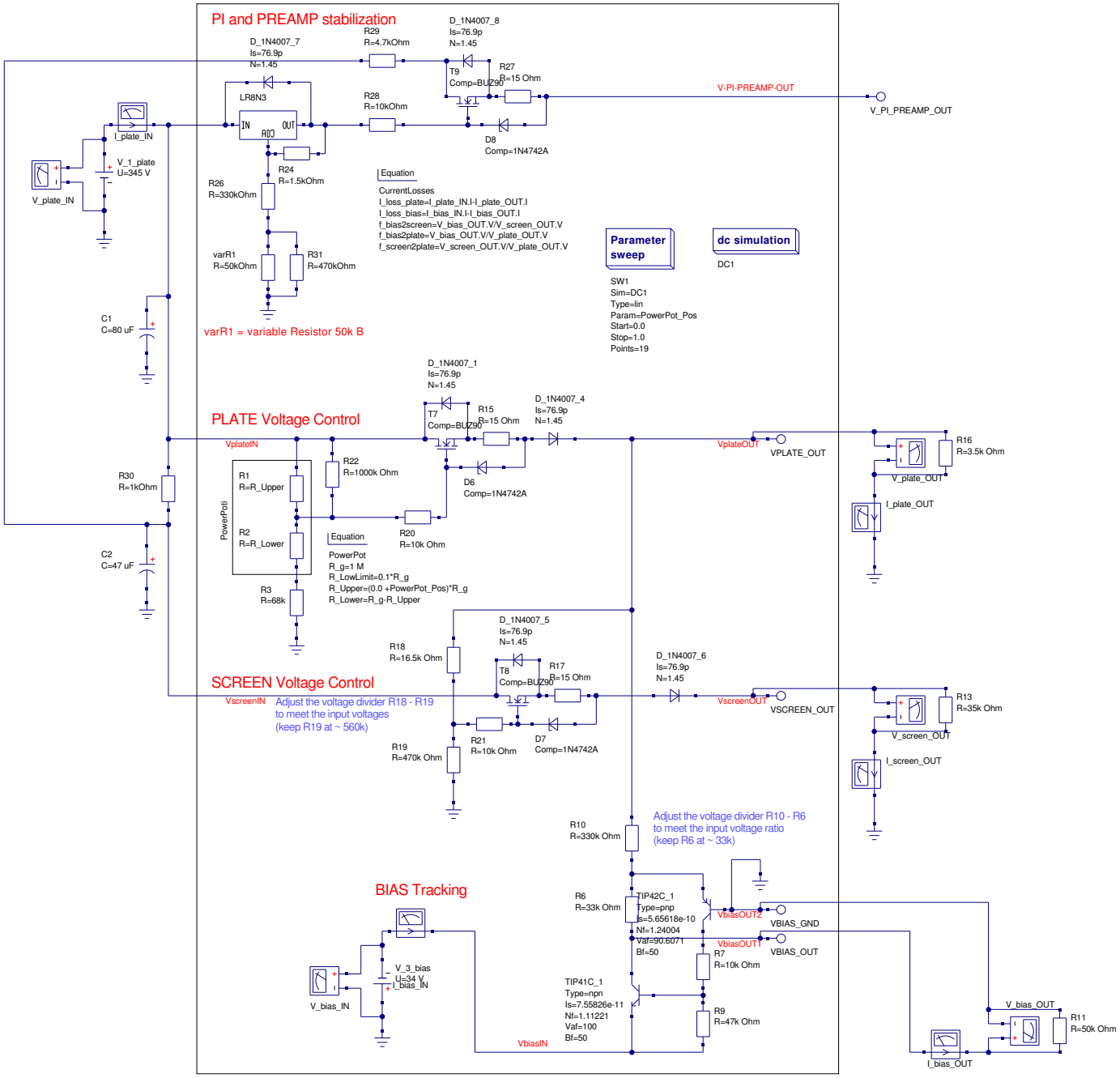
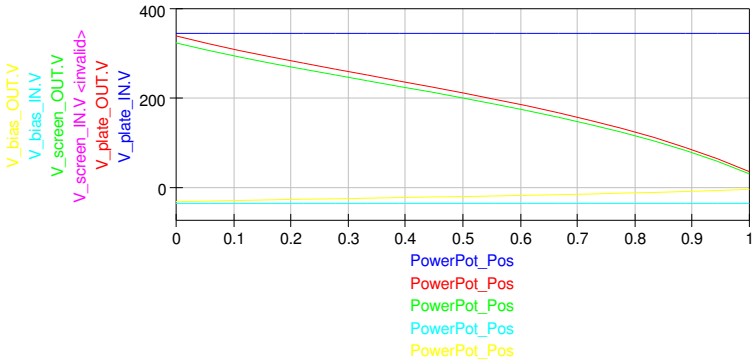
Preliminary Untested Circuit of VVR

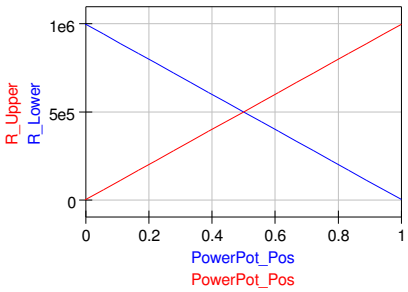


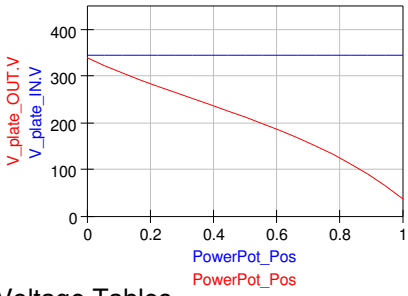
Summary of VVR DC Simulation

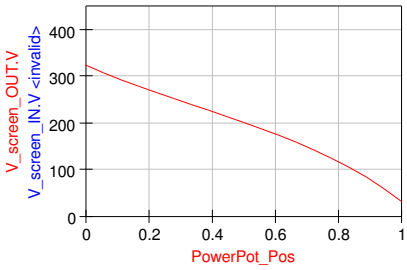
All data is displayed vs PowerPotentiometer position.
Warning: Circuit only simulated but not tested in real life application

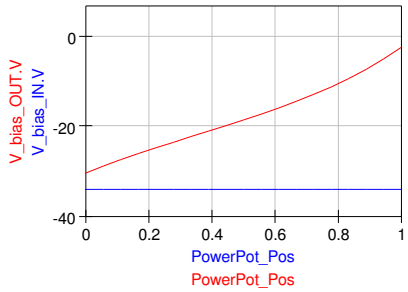
Overview on Voltages







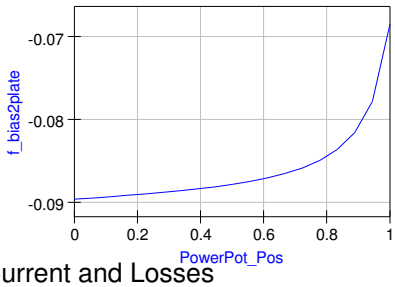


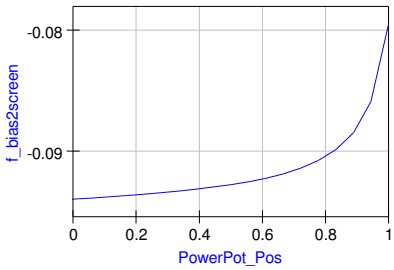


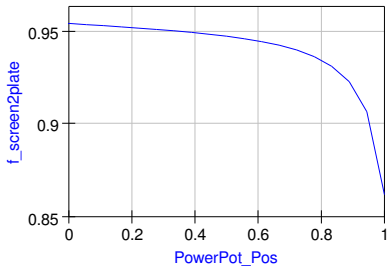
Voltage Tables

PowerPot_Pos	V_plate_IN.V	V_screen_IN.V	V_bias_IN.V	V_plate_OUT.V	V_screen_OUT.V	V_bias_OUT.V
0	345	no data	-34	339	324	-30.4
0.0556	345		-34	322	307	-28.8
0.111	345		-34	307	292	-27.4
0.167	345		-34	292	278	-26.1
0.222	345		-34	279	265	-24.8
0.278	345		-34	265	252	-23.6
0.333	345		-34	252	239	-22.3
0.389	345		-34	239	227	-21.1
0.444	345		-34	226	214	-19.9
0.5	345		-34	212	201	-18.6
0.556	345		-34	198	188	-17.3
0.611	345		-34	184	173	-16
0.667	345		-34	168	158	-14.5
0.722	345		-34	151	142	-13
0.778	345		-34	133	124	-11.3
0.833	345		-34	113	105	-9.44
0.889	345		-34	90.6	83.6	-7.4
0.944	345		-34	65.6	59.5	-5.11
1	345		-34	37.1	32	-2.55

Scaling Quality







Current and Losses

