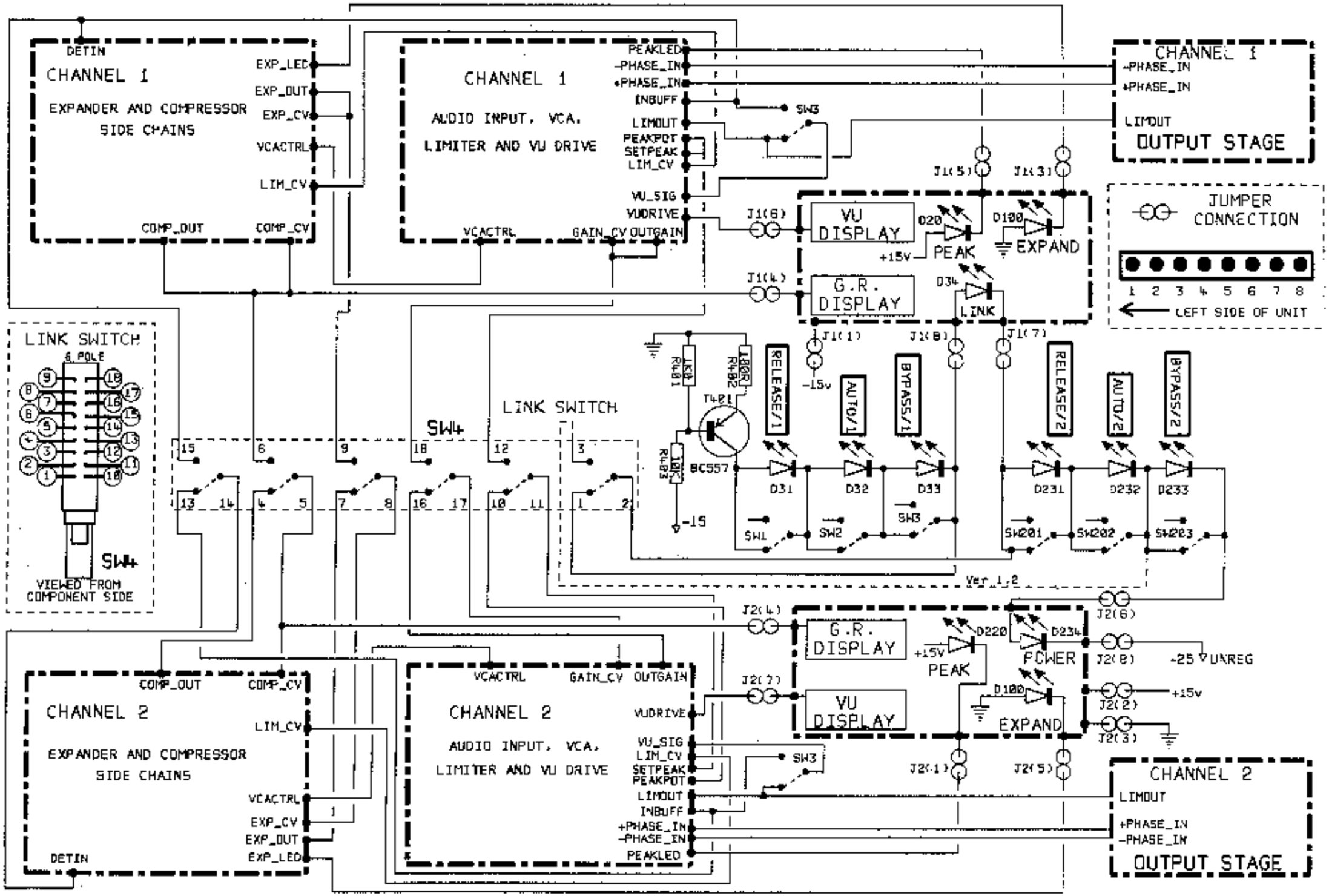


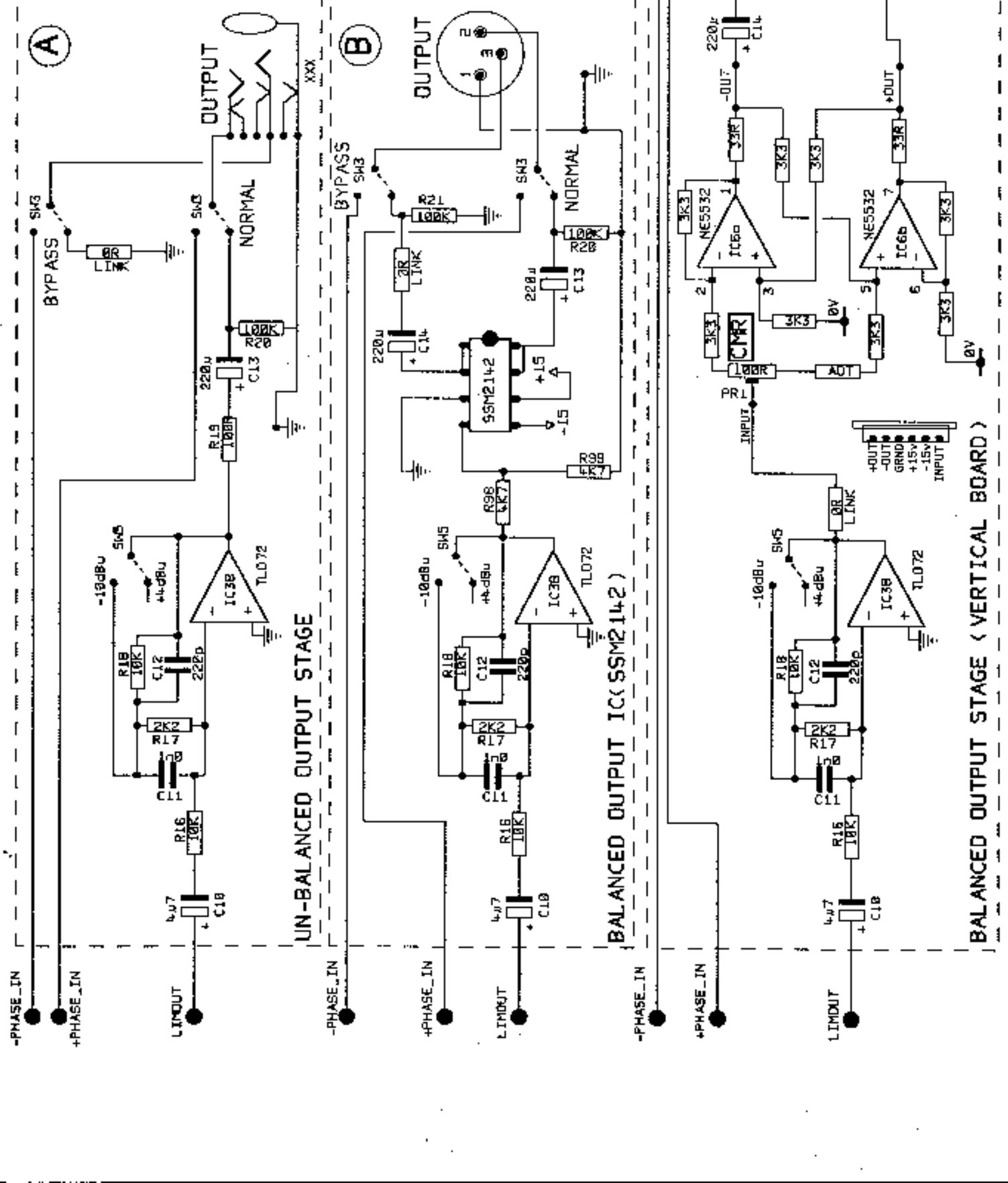
DL241 BLOCK DIAGRAM

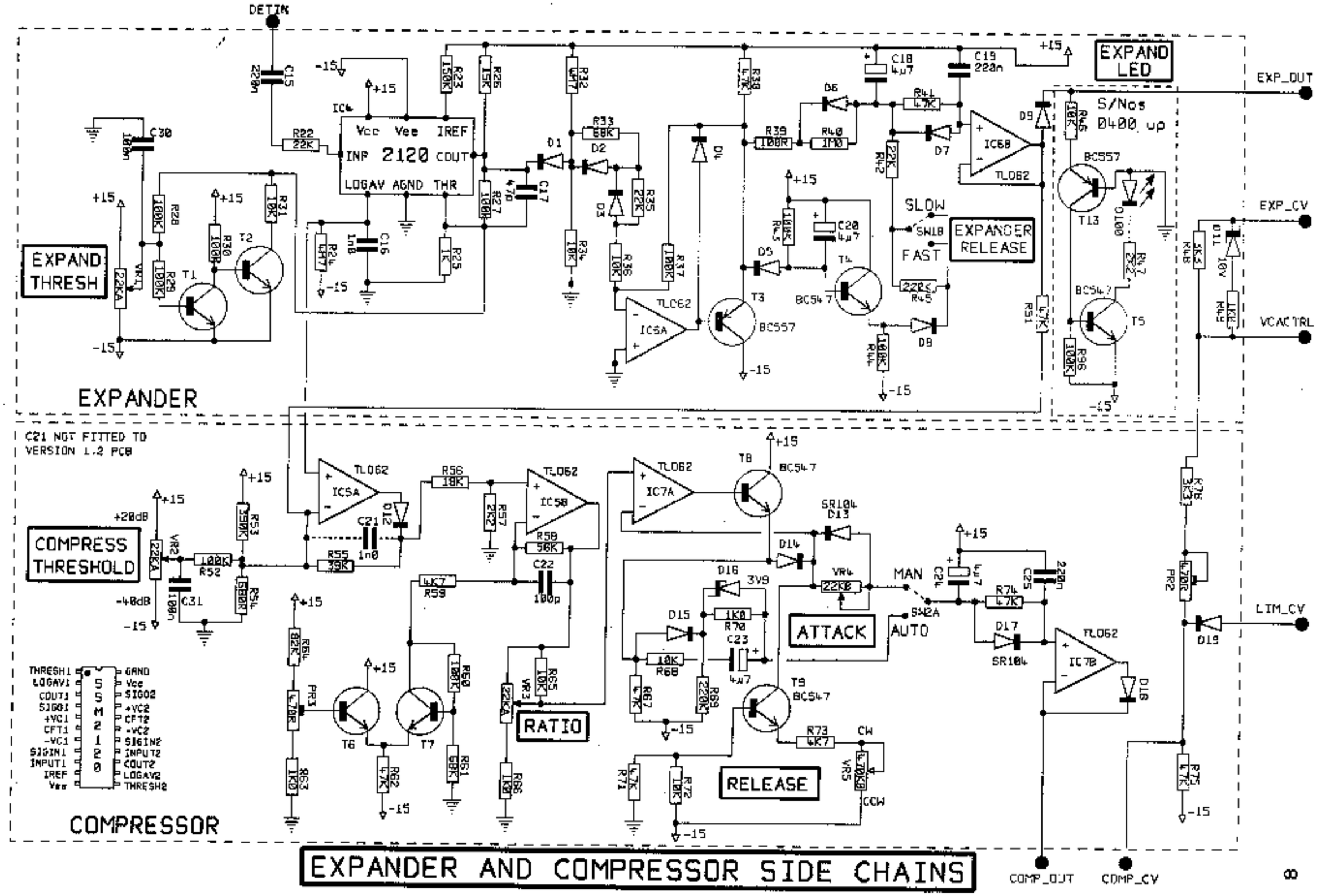
THREE OUTPUT STAGE CIRCUITS CAN BE FITTED TO THE DL241

OUTPUT STAGE (A) IS THE UNBALANCED VERSION
IT RETAINS COMPATIBILITY WITH BALANCED EQUIPMENT
BY SHORTING THE -PHASE (COLD) CONNECTION TO GROUND

OUTPUT STAGES (B) AND (C) ARE FULLY BALANCED
OUTPUT STAGE (B) USES THE SSM 2142 LINE DRIVER
OUTPUT STAGE (C) IS USED WHEN THE SSM 2142
IS UN-AVAILABLE

A BALANCED CONNECTION IS ALWAYS AVAILABLE IN BYPASS




DETIN
EXPAND THRESH
EXPANDER
EXPAND LED
S/Nos 0400 up
EXP_OUT
EXP_CV
VCACTRL
SLOW
FAST
EXPANDER RELEASE
C21 NOT FITTED TO VERSION 1.2 PCB
COMPRESS THRESHOLD
+20dB
-40dB
RATIO
ATTACK
AUTO
MAN
RELEASE
LIM_CV
COMP_OUT
COMP_CV
COMPRESSOR
EXPANDER AND COMPRESSOR SIDE CHAINS

AUDIO INPUT. VCA. LIMITER AND VU DRIVE

INPUT STAGE

J1 XXX
C1 4µ7
C2 100p
C3 22p
C4 4µ7
C5 100p
C6 22p
C7 4µ7
C8 100p
C9 47p
R1 1K0
R2 1K0
R3 20K
R4 20K
R5 82K
R6 20K
R7 20K
R8 82K
R9 15K
R10 150K
R11 12K
R12 4K7
R13 100R
R14 15K
IC1A TL072
IC2 DBX 2151
IC1B TL072
SW5 +4dBu -10dBu
SW5
SET THD
PR1 100K

PEAK CIRCUIT

R15 1K0
R80 1K0
D21
D22
D23
D24
D25
D26
D27 1/2 LM324
IC3a TL072
IC8a
IC8b
R77 2K2
R78 1K77
R79 1K0
R81 100R
R82 47K
R83 10K
R84 10K
C25 4µ7
C26 4µ7
C27 100n
C28 4µ7
T10 BC547
T11 BC547
+15
PEAK LEVEL
R86 3K3
VR7 22KA
R87 3K3
R85 1K5
PKLED
-PHASE_IN
+PHASE_IN
INBUFF
LIMOUT
PEAKPOT
SETPEAK
LIM_CV

VCA CIRCUIT

+15
-15
VCACTRL
VU_SIG

VU DRIVE

R88 39K
R89 47K
R90 51K
R91 47K
R92 47K
R93 1K0
R94 47K
R95 47K
C29 4µ7
D28
D29
D30
IC8d
IC8c
1/2 LM324
T12 BC547
VU_FEED
VUDRIVE

OUTPUT GAIN

R50 10K
C32 100n
VR6 22KA
GAIN_CV
OUTGAIN