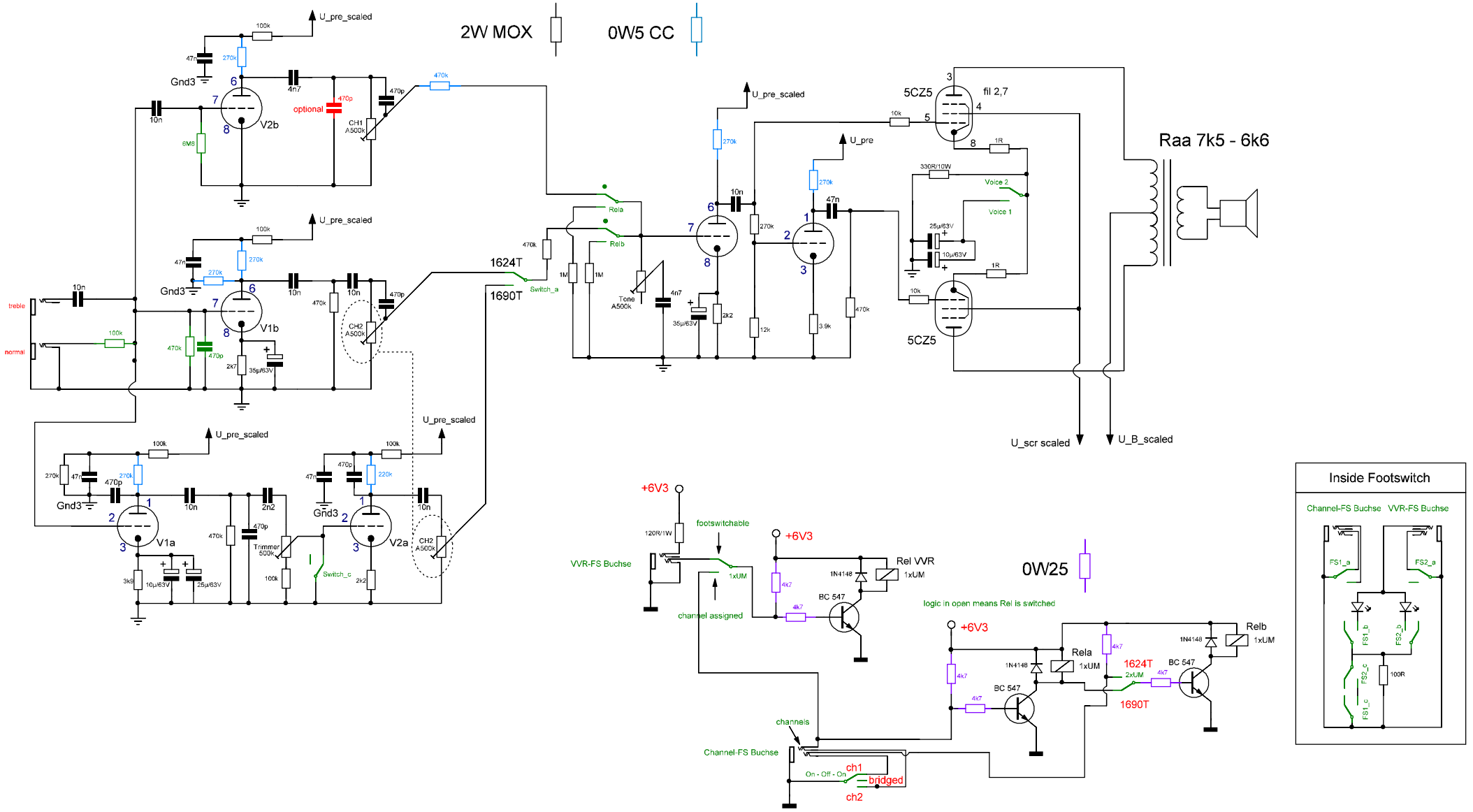
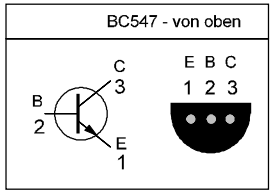




Truth table für die Logic

		Rela	Relb	Rel VVR assigned
1624T	ch1	on	off	on
	bridged	on	on	on
	ch2	off	on	off
1690T	ch1	on	off	on
	bridged	on	off	on
	ch2	off	on	off